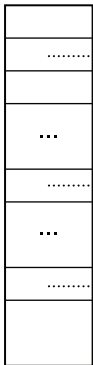
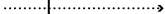


Processor
Register

Interrupt
Vector Table



.....→ `handleTimerInterrupt() {`
 ...
 }

.....→ `handleDivideByZero() {`
 ...
 }

.....→ `handleSystemCall() {`
 ...
 }