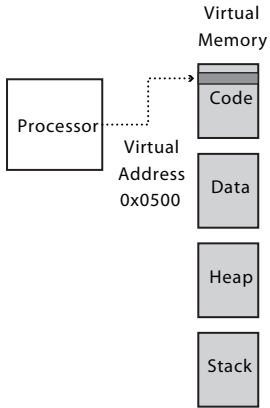
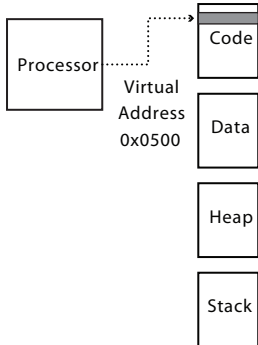


Processor's View

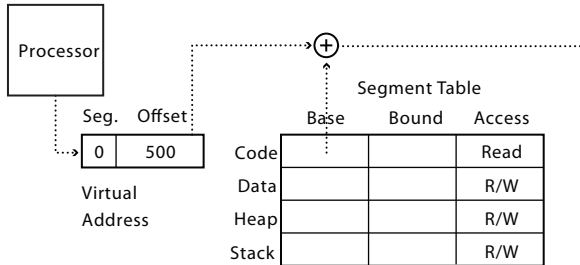
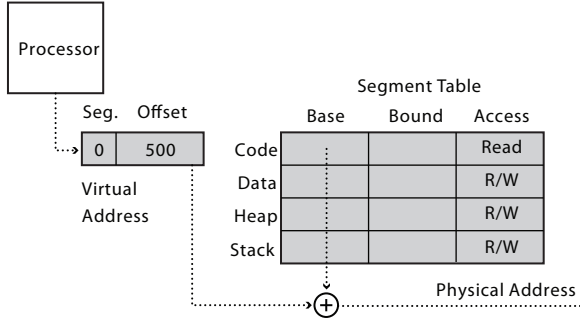
Process 1's View



Process 2's View



Implementation



Physical Memory

